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61 A new selective repeat scheme-actual environment performance analysis
Duarte, O.C.M.B.; de Lima, H.M.;

Communications, 1988. ICC 88. Digital Technology - Spanning the Universe. Conference Record. IEEE International Conference on , 12-15 June 1988
Pages:667 - 672 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(436 KB\)\]](#) **IEEE CNF**

62 A New Connection Establishment Procedure for Multidestination Protocols
Sabnani, K.; Schwartz, M.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 34 , Is 11 , Nov 1986
Pages:1161 - 1165

[\[Abstract\]](#) [\[PDF Full-Text \(568 KB\)\]](#) **IEEE JNL**

63 Peripheral board controller for digital exchange systems
Lerach, L.; Geiger, G.; Strafner, M.;

Solid-State Circuits, IEEE Journal of , Volume: 19 , Issue: 4 , Aug 1984
Pages:485 - 490

[\[Abstract\]](#) [\[PDF Full-Text \(1168 KB\)\]](#) **IEEE JNL**

64 A peripheral board controller for digital exchange systems
Lerach, L.; Geiger, G.; Strafner, M.;

Solid-State Circuits Conference. Digest of Technical Papers. 1983 IEEE International , Volume: XXVI , Feb 1983
Pages:76 - 77

[\[Abstract\]](#) [\[PDF Full-Text \(600 KB\)\]](#) **IEEE CNF**

65 Verification of HDLC
Brand, D.; Joyner, W., Jr.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 30 , Is 5 , May 1982

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1 Analysis of a statistics counter architecture

Devavrat Shah; Sundar Iyer; Balaji Prabhakar; McKeown, N.;
Hot Interconnects 9, 2001. , 22-24 Aug. 2001

Pages:107 - 111

[Abstract] [PDF Full-Text (376 KB)] IEEE CNF

2 A 2.5 V, 20 Gbyte/s 288 M packet-based DRAM with enhanced cell efficiency and noise immunity

Kyung, K.-H.; Lee, H.-C.; Song, K.-W.; Song, H.-S.; Jung, K.-W.; Lee, D.-Y.; C.; Cho, S.-I.;
VLSI Circuits, 2000. Digest of Technical Papers. 2000 Symposium on , 15-17 2000

Pages:112 - 115

[Abstract] [PDF Full-Text (456 KB)] IEEE CNF

3 A new memory controller for the shared multibuffer ATM switch with multicast functions

Chang, R.C.; Chih-Yuan Hsieh;
Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on , Volume: 6 , 31 May-3 June 1998
Pages:502 - 505 vol.6

[Abstract] [PDF Full-Text (308 KB)] IEEE CNF



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Adelman, L.; Miller, S.L.; Yeo, C.;

Systems, Man and Cybernetics, Part A, IEEE Transactions on , Volume: 34 , I 2 , March 2004

Pages:179 - 189

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **IEEE JNL****2 Implementation of an environment of symbolic model checking based on the TDGs**

Ayadi, S.; Lakhnech, Y.; Robbana, R.;

Computer Systems and Applications, 2003. Book of Abstracts. ACS/IEEE International Conference on , 14-18 July 2003

Pages:97

[\[Abstract\]](#) [\[PDF Full-Text \(172 KB\)\]](#) **IEEE CNF****3 Relative performance of preemption-safe locking and non-blocking synchronization on multiprogrammed shared memory multiprocessor**

Michael, M.M.; Scott, M.L.;

Parallel Processing Symposium, 1997. Proceedings., 11th International , 1-5 , 1997

Pages:267 - 273

[\[Abstract\]](#) [\[PDF Full-Text \(696 KB\)\]](#) **IEEE CNF****4 Tango: a hardware-based data prefetching technique for superscalar processors**

Pinter, S.S.; Yoaz, A.;

Microarchitecture, 1996. MICRO-29. Proceedings of the 29th Annual IEEE/ACM International Symposium on , 2-4 Dec. 1996

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